Electronic Patent Application Fee Transmittal								
Application Number:	09775167							
Filing Date:	01-Feb-2001							
Title of Invention:	Shift register circuit capable of reducing consumption of power with reduced capacitive load of clock signal line and image display device including it							
First Named Inventor/Applicant Name:	Yasushi Kubota							
Filer:	Steven M. Jensen							
Attorney Docket Number:	55561 (70820)							
Filed as Large Entity								
Utility Filing Fees								
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Basic Filing:								
Pages:								
Claims:								
Miscellaneous-Filing:								
Petition:								
Patent-Appeals-and-Interference:								
Filing a brief in support of an appeal		1402	1	510	510			
Post-Allowance-and-Post-Issuance:								
Extension-of-Time:								

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
	Total in USD (\$)			510